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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APP. NO.	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10003412	12-06-2001	422		2812	

**APPLICANTS: John J. Kim & Son, Inc.

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:
REPUBLIC OF KOREA 01-24045 05-03-2001

PG-PUB DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO.
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	249-276
Verified and Acknowledged Examiners initials	
TITLE : Apparatus for manufacturing semiconductor device	

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
ISSUE FEE		Sheets Drwg.	Figs. Drwg.
Amount Due	Date Paid	Print Fig.	
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 363, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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